



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Willson et al.

PATENT APPLICATION

Serial No.: 10/776,881

Group Art Unit: Unassigned

Filed: February 11, 2004

Examiner: Unassigned

For: AN APPARATUS FOR FABRICATING NANOSCALE PATTERNS IN  
LIGHT CURABLE COMPOSITIONS USING AN ELECTRIC FIELD

**DECLARATION OF ALEXIS SHEFFIELD IN SUPPORT OF PROOF OF  
MAILING UNDER 37 CFR § 1.8(b)**

Commissioner  
for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

1. I am over the age of Eighteen (18) years and a  
resident of the state of Texas.

2. I make this declaration in support of a Proof of  
Mailing Under 37 CFR section 1.10(e) that I am informed and  
believed will be filed herewith with the United States  
Patent & Trademark Office in the above-mentioned patent  
application.

3. At all times relevant herein I was employed in  
the Law Office of Kenneth C. Brooks as a patent  
administrator.

4. On February 11, 2004, I deposited an original  
patent application with the United States Postal Service as  
identified above in paragraph 2 employing the Express  
Mailing Procedure set forth in 37 CFR section 1.10(e) that  
included a Specification, including page 21 having claims  
23, 24, and 25 recited thereon, a copy of which is attached  
hereto as APPENDIX 1; the Utility Patent Application  
Transmittal, see APPENDIX 2; a Return postcard, see

**EXHIBIT A**

APPENDIX 3; and a copy of the Express Mail receipt showing a date of February 11, 2004, see APPENDIX 4.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fin, imprisonment or both, under Section 1001 of Title 18 of the United States code, and such willful false statements may jeopardize the validity of the application or any patent issuing there from.

Date: June 3, 2004

Alexis Sheffield  
ALEXIS SHEFFIELD

of voltage being in electrical communication with said layer of ITO.

23. The apparatus as recited in claim 19 wherein said template is substantially transparent to ultraviolet light.

24. The apparatus as recited in claim 19 wherein said template further includes a fluorine containing monolayer.

25. The apparatus as recited in claim 19 wherein template further includes a layer of Indium Tin Oxide (ITO), with said plurality protrusions of recessions are formed in said layer of ITO and further including a fluorine containing monolayer positioned adjacent to said layer of ITO, with said fluorine containing monolayer being positioned between said substrate and said layer of ITO.